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APPLICANTS

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** CONTINUING DATA *****

None *Ad*

** FOREIGN APPLICATIONS *****

GERMANY 103 06 281.5 02/14/2003 *Ad*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/11/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	GERMANY	DRAWING 13	CLAIMS 33	CLAIMS 4
Verified and Acknowledged <i>Andy Mays</i>	Examiner's Signature Initials				

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TITLE

Method of fabricating and architecture for vertical transistor cells and transistor-controlled memory cells

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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